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Inventor Name Search Result

Your Search was:

Last Name = PARVATHALA
First Name = PRAVEEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 7
10326723	Not Issued	030	12/20/2002	ENABLING AT SPEED APPLICATION OF TEST PATTERNS ASSOCIATED WITH A WIDE TESTER INTERFACE ON A LOW PIN COUNT TESTER	PARVATHALA, PRAVEEN K.
09922639	Not Issued	030	08/07/2001	TOOL FOR GENERATING A RE-GENERATIVE FUNCTIONAL TEST	PARVATHALA, PRAVEEN K.
09917661	Not Issued	030	07/31/2001	FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS	PARVATHALA, PRAVEEN K.
08979407	5978944	150	11/26/1997	METHOD AND APPARATUS FOR SCAN TESTING DYNAMIC CIRCUITS	PARVATHALA , PRAVEEN
08979195	5872795	150	11/26/1997	METHOD AND APPARATUS FOR SCAN TESTING OF MULTI-PHASE LOGIC	PARVATHALA , PRAVEEN
08831478	5968194	150	03/31/1997	METHOD FOR APPLICATION OF WEIGHTED RANDOM PATTERNS TO PARTIAL SCAN DESIGNS	PARVATHALA , PRAVEEN
08787824	6032278	150	12/26/1996	METHOD AND APPARATUS FOR PERFORMING SCAN TESTING	PARVATHALA , PRAVEEN

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Inventor Name Search Result

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Last Name = MANEPARAMBIL
First Name = KAILASNATH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 4
10326723	Not Issued	030	12/20/2002	ENABLING AT SPEED APPLICATION OF TEST PATTERNS ASSOCIATED WITH A WIDE TESTER INTERFACE ON A LOW PIN COUNT TESTER	MANEPARAMBIL, KAILASNATH S.
09922639	Not Issued	030	08/07/2001	TOOL FOR GENERATING A RE-GENERATIVE FUNCTIONAL TEST	MANEPARAMBIL, KAILASNATH
09917661	Not Issued	030	07/31/2001	FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS	MANEPARAMBIL, KAILASNATH
09474942	Not Issued	093	12/30/1999	METHOD AND APPARATUS FOR SCAN DESIGN USING A FORMAL VERIFICATION-BASED PROCESS	MANEPARAMBIL, KAILASNATH S.

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